L Number	Hits	Search Text	DB	Time stamp
1	76936	interface with(modul\$4 or block)	USPAT; US-PGPUB;	2003/01/30 10:11
2	1254	<pre>interface with(logic adj(modul\$4 or block))</pre>	EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/01/30 10:12
3	269	(chip or ic or semiconductor	DERWENT; IBM_TDB USPAT;	2003/01/30
		<pre>or(integrated adj circuit))same (interface with(logic adj(modul\$4 or block)))</pre>	US-PGPUB; EPO; JPO; DERWENT; IBM TDB	10:27
4	132	<pre>((chip or ic or semiconductor or(integrated adj circuit))same (interface with(logic adj(modul\$4 or block)))) and(translat\$3 or protocol)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/01/30 11:12
5	13060	(particular or propriet\$4 or special\$4 or specific)near2 logic	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/01/30 11:14
6	310	((particular or propriet\$4 or special\$4 or specific)near2 logic) near5 interfac\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/01/30 12:36
7	59	(((particular or propriet\$4 or special\$4 or specific)near2 logic) near5 interfac\$3) with bus	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/01/30 12:37
8	12018	(internal or chip)adj bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/01/30 11:25
9	15	((particular or propriet\$4 or special\$4 or specific)near2 logic) same ((internal or chip)adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/01/30 12:37
10	219	(virtual near3(socket or port))with interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/01/30 11:35
11	63	virtual adj2 socket	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/01/30 11:36
12	471	((particular or propriet\$4 or special\$4 or specific)near2 logic) with interfac\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/01/30 12:36
13	0	<pre>((((particular or propriet\$4 or special\$4 or specific)near2 logic) with interfac\$3) same ((internal or chip)adj bus))not (((particular or propriet\$4 or special\$4 or specific)near2 logic) same ((internal or chip)adj bus))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/30 12:37

14	46	((((particular or propriet\$4 or special\$4	USPAT;	2003/01/30
		or specific)near2 logic) with interfac\$3)	US-PGPUB;	12:46
		with bus)not ((((particular or propriet\$4	EPO; JPO;	
	}	or special\$4 or specific)near2 logic)	DERWENT;	
l		near5 interfac\$3) with bus)	IBM_TDB	/ /
15	155610	function\$2 or logic)adj2(block or unit or	USPAT;	2003/01/30
		module	US-PGPUB;	17:51
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	((
16	13366	interface with ((internal or system or	USPAT;	2003/01/30
		chip)adj bus)	US-PGPUB;	12:58
			EPO; JPO;	
			DERWENT;	1
4.7	00.607		IBM_TDB	0000 (01 (00
17	93637	(within or single or one)adj2(chip or	USPAT;	2003/01/30
		semiconduvtor or ic or(integrated adj	US-PGPUB;	13:01
		circuit))	EPO; JPO;	
			DERWENT;	
10	- 60	15 11 40 7 1 1 1 1 1 1 1 1 1	IBM_TDB	0002/01/20
18	62	(function\$2 or logic)adj2(block or unit	USPAT;	2003/01/30
}		or module) same (interface with((internal	US-PGPUB;	13:25
	1	or system or chip)adj bus)) same ((within	EPO; JPO;	
		or single or one)adj2(chip or	DERWENT;	
		semiconduvtor or ic or(integrated adj	IBM_TDB	
19	325	circuit)))	HCDAM.	2003/01/30
19	325	((interface with((internal or system or	USPAT;	14:19
	1	chip)adj bus)) same ((within or single or one)adj2(chip or semiconduvtor or ic	US-PGPUB;	14:19
			EPO; JPO;	!
		or(integrated adj circuit))))not	DERWENT;	l
	•	((function\$2 or logic)adj2(block or unit	IBM_TDB	
		or module) same (interface with((internal or system or chip)adj bus)) same ((within		
		or single or one)adj2(chip or		
		semiconduvtor or ic or(integrated adj		1
		circuit))))		
20	5913	(confgurab\$5 or reconfigur\$5 or	USPAT;	2003/01/30
20	3,13	programmab\$5)near4 interface	US-PGPUB;	15:11
		Programmas 40 / mear 1 meer acc	EPO; JPO;	10.11
			DERWENT;	
	:		IBM TDB	
21	136	((within or single or one)adj2(chip or	USPAT;	2003/01/30
		semiconduvtor or ic or (integrated adj	US-PGPUB;	14:50
		circuit))) same ((confgurab\$5 or	EPO; JPO;	- 1
		reconfigur\$5 or programmab\$5)near4	DERWENT;	
		interface)	IBM TDB	
22	1910	((configurab\$5 or reconfigur\$5 or	USPAT;	2003/01/30
	,	programmab\$5)near4(block or unit or	US-PGPUB;	15:29
		module or architecture)) with interface	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
23	369	((configurab\$5 or reconfigur\$5 or	USPAT;	2003/01/30
		programmab\$5)near4(block or unit or	US-PGPUB;	15:15
		module or architectu22 with busre))with	EPO; JPO;	
		interface	DERWENT;	
			IBM_TDB	
24	49	((within or single or one)adj2(chip or	USPAT;	2003/01/30
		semiconduvtor or ic or(integrated adj	US-PGPUB;	15:26
		circuit))) and (((configurab\$5 or	EPO; JPO;	
ŀ		reconfigur\$5 or programmab\$5)near4(block	DERWENT;	
		or unit or module or architectu22 with	IBM_TDB	
	1	busre))with interface)		
25	125586	block or unit or modul\$2)near5(staging or	USPAT;	2003/01/30
1		stage or pipe or pipelin\$3 or cascad\$4	US-PGPUB;	18:17
			EPO; JPO;]
			DERWENT;	
L			IBM TDB	

26	9	(((configurab\$5 or reconfigur\$5 or	USPAT;	2003/01/30
		programmab\$5)near4(block or unit or	US-PGPUB;	15:39
		module or architecture)) with interface)	EPO; JPO;	
		same (block or unit or	DERWENT;	
		modul\$2)near5(staging or stage or pipe or	IBM_TDB	
07	2000	pipelin\$3 or cascad\$4)	110 D 3 M -	2003/01/20
27	2890	system adj chip	USPAT; US-PGPUB;	2003/01/30 15:42
			EPO; JPO;	15:42
			DERWENT;	
			IBM TDB	
28	151	(interface with((internal or system or	USPAT;	2003/01/30
		chip)adj bus)) and (system adj chip)	US-PGPUB;	17:49
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
29	1		USPAT	2003/01/30
				16:05
30	1		USPAT	2003/01/30
				16:05
31	1		USPAT	2003/01/30
32	1		USPAT	16:06 2003/01/30
32	1		USPAI	16:06
33	949	(function\$2 or logic)adj2(block or unit	USPAT;	2003/01/30
]]]	or module) with (bus adj interface)	US-PGPUB;	17:52
			EPO; JPO;	
			DERWENT;	
]			IBM_TDB	
34	206		USPAT;	2003/01/30
		or module) with(bus adj interface)) and	US-PGPUB;	17:56
		710/\$3.ccls.	EPO; JPO;	
			DERWENT;	
25	2	H20020144216H	IBM_TDB	2002/01/20
35		"20020144216"	USPAT; US-PGPUB;	2003/01/30 17:56
			EPO; JPO;	17.50
			DERWENT;	
			IBM TDB	
36	7723	(staging or stage or pipe or pipelin\$3 or	USPAT;	2003/01/30
		cascad\$4 or series) with (function\$2 or	US-PGPUB;	18:19
		logic)adj2(block or unit or module)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	0000 /01 /00
37	12		USPAT;	2003/01/30
		chip)adj bus)) same ((staging or stage or		18:19
		pipe or pipelin\$3 or cascad\$4 or series)with (function\$2 or	EPO; JPO; DERWENT;	
		logic)adj2(block or unit or module))	IBM TDB	
38	12		USPAT;	2003/01/30
	12	or cascad\$4 or series) with (function\$2 or	US-PGPUB;	18:35
		logic)adj2(block or unit or module))	EPO; JPO;	
		with(bus adj interface)	DERWENT;	
			IBM_TDB	
39	186		USPAT;	2003/01/30
		use\$4)or reconfigur\$4 or(re adj	US-PGPUB;	18:39
		configur\$4))	EPO; JPO;	
			DERWENT;	
L			IBM_TDB	